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A -78dBm Sensitivity Super-regenerative Receiver at 96 GHz with Quench-controlled Metamaterial Oscillator in 65nm CMOS

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Abstract — One high-sensitivity CMOS super-regenerative receiver is demonstrated for 96GHz mm-wave imaging based on high-Q metamaterial oscillator. Compared to traditional LC-tank based oscillator, the metamaterial oscillator is developed by folded-differential transmission-line loaded complementary split-ring resonator (FDTL-CSRR). With formed sharp stop-band, standing-wave is established with high EM-energy storage at mm-wave region for high-Q oscillatory amplification. As such, one high-sensitivity 96 GHz super-regenerative receiver is realized in 65nm CMOS with measurement results of: -78 dBm sensitivity, 0.67 fW/Hz^{0.5} NEP, 8.5 dB NF, 2.8mW power consumption and 0.014 mm² core area.

Index Terms — metamaterial oscillator, high Q and high sensitivity, super-regenerative receiver, mm-wave imaging.

I. INTRODUCTION

Recent researches [1]-[4] have demonstrated great potential of both active and passive millimeter-wave (mm-wave) imaging system to detect covered substances for either bio-medical or security detections. Compared to the other semiconductor technologies like GaAs, the standard CMOS technology is considered as the most cost-effective solution for large detector-arrayed mm-wave imaging system with on-chip image processing. However, due to loss in propagation path (80-90 dB) under limited transmitting power, the CMOS mm-wave imaging system requires sufficient receiver sensitivity.

The receiver sensitivity is determined by the minimum input signal power as described by Rayleigh-Hean thermal noise model: $S = KTB \cdot NF$. Here, S is the sensitivity, K is the Boltzmann constant, T is the ambient temperature (K), B is the bandwidth and NF is the noise figure. As such, a sharper bandwidth B under a given noise figure NF can result in a high sensitivity (or smaller S) receiver. Due to low frame-rate of mm-wave imaging video, the narrow or sharp bandwidth B is desired for high sensitivity of receiver. As such, the super-regenerative receiver is a promising candidate for mm-wave imaging applications [1][2] since high-sensitivity can be obtained by a quench-controlled oscillator to achieve an oscillatory amplification with sharper bandwidth than direct conversion receivers[3][4]. As illustrated in Fig.1, the quench-controlled oscillator consists of one resonator block and one frequency-selected positive feedback network. When periodical quench signal is applied, the

Fig.2. Super-regenerative receiver block diagram and operation principle.

Fig. 1. (a) On-chip implementation of T-line loaded with CSRR, (b) T-line model, (c) Unit-cell equivalent circuit of T-line loaded with CSRR

input signal power.

Note that the bandwidth B of one super-regenerative receiver is mainly determined by the quality factor (Q) of the resonator by: $Q = f_0/B$, where f_0 is the resonance frequency. As such, on-chip high-Q resonator is desired for the high-sensitivity super-regenerative receiver design. As shown in this paper, one metamaterial resonator is developed based on a novel structure of folded-differential transmission-line loaded with complementary split-ring resonator (FDTL-CSRR), which is further deployed in the

super-regenerative receiver for high-sensitivity. It is demonstrated with the measured -78dBm sensitivity at 96GHz in UMC 65nm CMOS.

II. HIGH-Q FDTL-CSRR OSCILLATOR

Metamaterial was first demonstrated in 2001 [5] with split-ring resonator (SRR), which shows negative permeability (μ) at resonance frequency, and is deployed for on-chip application in [6]. According to the complementary theory, complementary split-ring resonator (CSRR) was also proposed in 2004 [7] with negative permittivity (ϵ) at resonance frequency. The CSRR structure can be considered as an electric dipole excited by the electric field (E-Field) along the ring axis. CSRR with negative ϵ can be deployed as the load for a host with positive μ such as a transmission-line (T-line). As such, the electric plasma can be formed when ϵ is negative and μ is positive with a sharp stop-band at the vicinity of resonance. As such, the wave propagating at the host T-line is perfectly reflected at the CSSR load with an established standing-wave. Therefore, the EM-energy can be stored with low-loss transferring between the host and load, which results a high-Q resonator.

A. T-line loaded with CSRR

Note that for the on-chip implementation of T-line loaded with CSRR (TL-CSRR), CSRRs are engraved on the signal trace of T-line on the top most metal layer as shown in Fig. 2a. The metamaterial property for TL-CSRR can be analyzed by T-line model [8].

T-line is modeled by cascading unit-cells with distributed series impedance (Z) and shunt admittance (Y), which determine permittivity (ϵ) and permeability (μ) as depicted in Fig. 2b. The equivalent circuit of TL-CSRR is depicted in Fig. 2c. L and C are the intrinsic series inductance and shunt capacitance of T-line, while L_c and C_c are the equivalent inductance and capacitance of CSRR resonator. By comparing Fig. 2b and Fig. 2c, one can observe that μ is always positive as $\mu \propto L/d$, where d is the length of TL-CSRR unit-cell; and ϵ is negative when $\epsilon \propto (\frac{C_c}{\omega^2 L_c C_c} + C)/d < 0$, of which the frequency range is

$$\sqrt{\frac{1}{(C + C_c)L_c}} < \omega < \sqrt{\frac{1}{C_c L_c}} \quad (1)$$

As such, electric plasma is formed in this frequency range. The EM-energy propagating into the TL-CSRR will be reflected with established standing wave. The reflection coefficient ($|\Gamma|$) depends on the number of cascading TL-CSRR unit-cells and also the Q value of loaded CSRR.

Fig. 3. Reflection coefficient of T-line loaded with CSRR unit-cells

Fig. 4. Layout for on-chip implementation of FDTL-CSRR

Based on circuit level simulation, Fig.3 shows the study of TL-CSRR at 100 GHz resonance frequency with following observations. First, the reflection coefficient $|\Gamma|$ can be improved by high-Q loaded CSRRs. Second, for a given Q value of loaded CSRR, the overall reflection coefficient can be improved by cascading more unit-cells.

B. Folded Differential T-line loaded with CSRR

The TL-CSRR structure cannot be directly used for a super-regenerative receiver design due to the following reasons. First, the single-ended approach is not preferred because of large common-mode noise; second, additional area overhead is required when cascading more unit-cells.

To address these two issues, a folded-differential T-line loaded with CSRR (FDTL-CSRR) structure is proposed in this paper. As shown in Fig. 4, two cascaded CSRR unit-cells are folded into the top most metal layers (AL and M6) and are connected by AL vias in between. The ADS momentum EM-simulator is used to verify the S-parameters of the proposed FDTL-CSRR structure in Fig. 4. The metamaterial properties are extracted from the EM simulation results according to the procedure in [9]. Note that only the real parts of extracted ϵ and μ are considered in the analysis as the imaginary parts are introduced by the loss of T-line. As shown in Fig. 5, the electric plasma is formed with negative ϵ and positive μ at the vicinity of 104 GHz resonance. It forms a band with deep rejection of -36 dB and narrow bandwidth of 0.44 GHz. The Q factor can be calculated as 236 by: $Q = \omega_0 / \omega_{3dB}$.

Fig. 5. Characterization of FDTL-CSRR by EM-simulation

III. HIGH SENSITIVITY SUPER-REGENERATIVE RECEIVER

The high-Q FDTL-CSRR metamaterial resonator results in sharp bandwidth that can improve sensitivity of the super-regenerative receiver. One super-regenerative receiver with DTL-CSRR is designed in UMC standard 65 nm CMOS process to demonstrate this sensitivity improvement. The schematic of the proposed receiver is illustrated in Fig. 7. Firstly, FDTL-CSRR with the unit-cell size of $60 \mu\text{m} \times 60 \mu\text{m}$ is connected to cross-coupled M2 and M3 NMOS transistors to form a differential oscillator. The tail current of the oscillator is quench-controlled by M4 NMOS transistor. In order to provide a sufficient driving current and head-room for the oscillation, the size of M4 NMOS transistor is designed 4 times larger than M2 or M3 NMOS transistor. Note that the minimum channel length of 60nm is applied for all transistors.

The remaining circuit consists of one input low-noise-amplifier (LNA) and also one envelop detector. The M1 NMOS transistor serves as a common-source LNA and also provides reverse isolation between the RF input and the oscillator. The input of M1 NMOS transistor is matched to 50 Ω by T-line-based matching network. Note that the size of M1 NMOS transistor is much smaller than that of M2 or M3 NMOS transistors to avoid unbalanced oscillation. Moreover, M5 and M6 are the only PMOS transistors used in this circuit to serve as a differential envelop detector. Their sizes need to be small to reduce the capacitive loading of the oscillator. In order to isolate $1/f$ noise and reduce the capacitance loading from M5 and M6, the differential oscillation signal is capacitively coupled to the envelop detector by MOM capacitors C1 and C2. This also enables the gates of M5 and M6 to be biased externally, which brings the flexibility of output reference tuning and also detection optimization. In addition, there are two $\lambda/4$ stubs at 96 GHz connected to the output, which can improve the isolation of the oscillation signal.

Fig. 6. Schematic of super-regenerative receiver with FDTL-CSRR oscillator

IV. MEASUREMENTS AND DISCUSSION

As shown in Fig. 6, the super-regenerative receiver with FDTL-CSRR was fabricated in UMC 1P6M bulk 65 nm CMOS process. The core chip area is 0.014 mm^2 ; and the total area is $500 \mu\text{m} \times 440 \mu\text{m}$ including all testing PADs. The receiver is measured on CASCADE Microtech Elite-300 probe station. The RF signal is provided by Agilent PNA-X (N5247A) with T/R module (N5260). A 12.5MHz sinusoid quench-control signal is applied by function generator (AFG3022) with voltage swept in 0~250 mV. The receiver operates under 1V power supply and the power consumption is observed as 2.8 mW.

Good input power matching is also important for the NF reduction and sensitivity improvement. The measured S11 remains below -10 dB from 81 GHz to 104 GHz as shown in Fig. 9. As a result of utilizing the high-Q FDTL-CSRR, narrow bandwidth of 560 MHz is observed around the center frequency of 95.5 GHz. Moreover, an almost linear response is observed in the plot of normalized V_{out} versus input power (P_{in}) by Fig. 9.

In addition as shown in Fig.9, the sensitivity of receiver is found to be -78 dBm in the measurement. The noise equivalent power (NEP) is defined as signal power in one hertz bandwidth that gives unity signal-to-noise ratio, which is equivalent to $S/\sqrt{B}$ and is found to be $0.67 \text{ fW/Hz}^{0.5}$. Finally, the noise figure is found to be 8.5 dB by S/KTB at room temperature of 290 K.

Fig. 7. Chip photo of 96GHz super-regenerative receiver

TABLE I
PERFORMANCE COMPARISON WITH RECENTLY PUBLISHED
RECEIVERS ABOVE 90 GHz

	[3]	[4]	[1]	[2]	This work
Technology	65nm CMOS	65nm BiCMOS	65nm CMOS	65nm CMOS	65nm CMOS
Frequency (GHz)	94	94	144	183	95.5
Sensitivity (dBm)	-66	-57	-74	-72.5	-78
Noise Figure(dB)	N/A	12	10.2	9.9	8.5
Bandwidth (GHz)	23	26	0.94	1.4	0.56
NEP (fW/Hz ^{0.5})	N/A	10.4	1.3	1.51	0.67

Fig. 8. Measurement results of super-receiver: i) output voltage when a -20 dBm tone is swept through bandwidth; ii) and also input S11 parameters

Fig. 9. Vout of super-receiver when a 95.5 GHz tone is swept from sensitivity level to -13 dBm

As shown in TABLE I, the measurement results of the proposed receiver are compared to the recently published mm-wave imaging receivers above 94 GHz [1-4]. The proposed receiver has achieved the best sensitivity, noise figure and NEP with almost the smallest power consumption and also the core chip area. Specifically, compared to the results from LC-tank resonator based super-regenerative receiver in [1], our high-Q

metamaterial oscillator by FDL-CSRR has achieved more than 40% sharper bandwidth with 4dB higher sensitivity.

V. CONCLUSION

One high-sensitivity 96GHz super-regenerative receiver is demonstrated in 65nm CMOS with the use of metamaterial based high-Q resonator (FDTL-CSRR). Compared to the traditional LC-tank resonator, the high-Q and compact FDTL-CSRR results in narrower bandwidth and improved sensitivity for the super-regenerative receiver, which is implemented in UMC 65nm CMOS with 0.014 mm² of the core chip area. Measurement results show that the receiver has achieved: -78 dBm sensitivity, 0.67 fW/Hz^{0.5} NEP, 8.5 dB NF and 2.8 mW power consumption. The compact size with improved sensitivity is ideal for the application of large-arrayed mm-wave imaging in CMOS.

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