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Reconfigurable self-sufficient traps for ultracold atoms based on a superconducting square

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We report on the trapping of ultracold atoms in the magnetic field formed entirely by persistent supercurrents induced in a thin film type-II superconducting square. The supercurrents are carried by vortices induced in the two-dimensional (2D) structure by applying two magnetic field pulses of varying amplitude perpendicular to its surface. This results in a self-sufficient quadrupole trap that does not require any externally applied fields. We investigate the trapping parameters for different supercurrent distributions. Furthermore, to demonstrate possible applications of these types of supercurrent traps we show how a central quadrupole trap can be split into four traps by use of a bias field, as predicted by Zhang *et al.* [*Phys. Rev. A* **85**, 013404 (2012)].

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I. INTRODUCTION

Since their first demonstration in 2000 [1], atom chips have been envisioned to do for matter wave physics what conventional chips have done for electronics. Atoms would be coherently manipulated and steered by microscopic wire patterns on the chip surface. While atom chips have managed to fulfill many of these promises, it has since become apparent that they also bring with them some complications. For chips to manipulate atoms on the micrometer scale they need to confine the atoms close to the wires and the chip surface. This makes the atomic sample susceptible to both current noise in the chip wires and thermal Johnson noise, causing the coherence and trap lifetime of the atoms to be drastically reduced as they are brought closer to the chip [2,3]. Condensates transported in wire guides on atom chips tend to quickly fragment due to wire roughness [4–6], making it challenging to carry out matter-wave interference experiments on-chip. While some of these problems can be solved by adding more complexity to the currents [7] or to the chip design [8], it is clear that they present a significant experimental barrier for the realization of complex on-chip matter-wave manipulation.

In contrast to conventional atom chips, superconducting atom chips promise to solve some of these problems through a significant reduction in the Johnson noise close to the surface [9–11]. While they have been shown to improve magnetic trap lifetimes close to the chip surface, they can still suffer from the problem of technical current noise originating from any external power supply controlling the current through the chip wires [12,13]. To produce the long coherence times desirable in many ultracold atom experiments, it therefore becomes necessary to reduce or even eliminate noise from external current sources. One potential solution to this problem is to fabricate the superconducting wires on the chip out of type-II superconducting material. Due to the presence of the mixed state in the phase diagram of these materials, persistent supercurrents can be induced in them by subjecting them to a change in the external magnetic field between the first and second critical fields. These induced supercurrents have

been shown to be strong enough to form atomic traps with an additional bias field [14–16].

In this paper we report on the production and loading of a magnetic trap for ultracold atoms formed entirely by persistent supercurrents induced in a superconducting square. We discuss the magnetic field pulse sequence required to form such a self-sufficient trap and give an estimate of its trap depth based on the transfer efficiency from an external quadrupole trap. To demonstrate the versatility of using fields patterned into a type-II superconductor on a chip we show that the fields induced into the same square can act as a four-way beamsplitter for the atoms, controllable by an external bias field. The methods outlined in this paper may therefore be implemented in designing future low-noise on-chip atom interferometer experiments.

II. SETUP

The atom chip used in the experiment uses a 800 nm thick layer of YBCO as the superconducting material with a critical temperature of ~ 87 K patterned onto an yttria stabilized zirconia (YSZ) substrate (Fig. 1). The chip is cooled to 83 K resulting in a critical current density of $J_c = 1$ MA/cm². A 1 mm $\times$ 1 mm square of YBCO is deposited onto the substrate without any leads connecting the structure to an external power source. Once cooled below the superconducting phase transition, vortices are loaded into the square by applying field pulses perpendicular to its surface. The strength of each field pulse B_{pulse} is chosen to be greater than the first critical field of the superconductor but below the second critical field ($B_{c1} < B_{\text{pulse}} < B_{c2}$). The pattern of the supercurrent induced in the square by this method is governed by the geometry of the superconductor as well as the sequence of loading pulses and their magnitude and direction [17]. The inset of Fig. 1 shows atoms trapped in a quadrupole trap generated by an external bias field and the magnetic field of supercurrents induced by a single loading pulse.

Up to 10^8 ⁸⁷Rb atoms are collected in a six-axis magneto-optical trap (MOT) loaded from light induced atomic desorption (LIAD) (Fig. 2). After MOT compression, molasses, and optical pumping, the atoms are trapped in the $|F = 2, m_F = 2\rangle$ state by the magnetic field of the MOT coils. The 35 mm transfer to the chip is accomplished by ramping down the MOT

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FIG. 1. (Color online) Concept of loading supercurrents into a type-II superconducting square. (a) Magnetic field pulses are applied perpendicular to the square surface (B_{pulse}), inducing vortices in the structure. The vortices carry a supercurrent I giving rise to a magnetic field reflecting the current flow on the surface. Inset: Cloud of ^{87}Rb trapped in a quadrupole field produced by the magnetic field from the square and a bias field $B_{\text{bias}} = 24.5$ G. A single field pulse $B_{\text{pulse}} = 278$ G was used to load supercurrents into the square (shaded area). The image is smoothed with a Gaussian filter ($\sigma = 6.5 \mu\text{m}$) to reduce pixel noise.

coil current while increasing the current through a second pair of “transfer coils.” Three sets of Helmholtz coil pairs allow for translation of the magnetic trap along all three axes in order to center the trap on the superconducting square. The magnetic field pulses used to imprint vortex patterns into the square are generated by applying a current of up to 274 A to the MOT coils for a duration of 100 ms prior to loading the MOT, corresponding to a field at the chip surface of 600 G. Increasing the amplitude of a pulse increases the penetration depth of the magnetic field into the superconductor, giving control over the magnitude and radius of the induced current loop. Choosing the polarity of the current applied to the MOT coils controls

FIG. 2. (Color online) The experimental components used to prepare and transport ultracold ^{87}Rb atoms to the chip. Atoms are loaded into a magneto-optical trap (MOT), cooled by molasses, and trapped in a quadrupole trap after optical pumping. A second set of quadrupole coils transfers the atoms close to the chip where they can be moved in all directions with the use of three Helmholtz pairs (compensating coils). For clarity the third set of compensating coils is not shown.

FIG. 3. (Color online) Absorption image of ultracold atoms in the vicinity of the superconducting square. The square carries supercurrents loaded by two pulses: $B_{\text{pulse}} = 600$ G and $B_{\text{pulse}} = -226$ G. The induced magnetic field along x at the surface of the square according to Bean's model is shown in black where L is the length of the square. The atoms display a triangular structure closely resembling the magnetic field once they are close to the superconducting surface. The imaging angle is identical to that in the inset of Fig. 1. The image is smoothed with a Gaussian filter ($\sigma = 6.5 \mu\text{m}$) to reduce pixel noise.

the direction of the supercurrent. The geometry of the current mimics the shape of the superconductor. The induced currents have previously been measured to be stable over the course of 3 h [15], requiring the pulse loading process to only be carried out once during the course of the experiment. Atoms are brought closer to the chip by increasing the bias field in the direction normal to its surface. Once the gradients generated by the supercurrents in the square become comparable to those generated by the transfer coils, the atomic distribution in the trap is considerably modified by the chip field. In this regime the spatial atomic density distribution reflects the profile of the magnetic field generated by the square, and one can map out the field distribution above the superconductor through absorption imaging of the atoms. Figure 3 shows the optical density of the atoms in the vicinity of the superconducting square's surface after two magnetic loading pulses: $B_{\text{pulse}} = 600$ G followed by $B_{\text{pulse}} = -226$ G. The magnetic field structure as predicted by Bean's model [18] is clearly reflected in the atomic distribution (Fig. 3 inset). In this way cold atoms may in the future even be used to image the vortex lattice of the superconductor much as it is currently done with magneto-optical imaging [19].

III. RESULTS

A quadrupole trap above the square center can easily be created by loading vortices into the square with a single magnetic field pulse and superimposing an external bias field on the chip field (Fig. 1). Such a trap still suffers however from field noise arising from current fluctuations in the coils providing the external field. To produce a truly self-sufficient trap the role of the external bias field must be assumed by additional supercurrents induced into the square [17]. Since the role of the bias field is to cancel out the field from the

FIG. 4. (Color online) Atoms trapped in the magnetic field created by a loading pulse sequence $B_{\text{pulse}} = 600$ G followed by $B_{\text{pulse}} = -226$ G. The trapping fields are derived entirely from persistent supercurrents on the chip. The bottom cloud is a mirror image of the trapped atoms resulting from the reflective chip surface. The image is smoothed with a Gaussian filter ($\sigma = 6.5$ μm) to reduce pixel noise.

square at a certain height $z = z_0$, a second loading field pulse in the direction opposite of the first should induce currents into the square that produce a similar “bias field.”

Figure 4 shows a cloud of 10^3 ultracold atoms trapped in a self-sufficient quadrupole trap created by such a two-pulse sequence. The distance to the chip surface is ~ 155 μm . The trap is loaded by moving the center of the magnetic trap produced by the transfer coils to the self-sufficient trap center in 300 ms, at which point all external fields are switched off in 1 ms. To determine the precise location of the self-sufficient trap center we make use of the field-mapping technique described in Fig. 3 to find the minimum in $|B|$. The atom number in the trap displays a fast (50 ms) initial decay on top of a longer lifetime of 1.5 s (Fig. 5). We attribute the fast initial decay of the atom number to loss of high energy atoms hitting the chip surface. Numerical estimates of the self-sufficient trap depth, as well as evaluations based on the fraction of atoms trapped, give a trap depth of about 10 μK . The temperature of the atoms loaded into the trap on the other

FIG. 5. (Color online) Lifetime of the atoms in the self-sufficient trap. The fast initial loss of atoms arises from the fact that the loaded atoms have a temperature higher than the trap depth of 10 μK . The decay rate after the initial loss is in agreement with the expected Majorana spin-flip rate in the trap [20]. The data are fit to a sum of two exponential decays with no offset.

hand is 200 μK . The longer, 1.5 s lifetime in the trap agrees well with the expected Majorana spin-flip rate of the atoms [20] and could be increased by producing traps on larger squares and by increasing the critical current density by cooling the superconductor further. Following a different route it is also feasible to overcome spin flip losses by dressing the atoms with a radio-frequency field to remove the zero of the magnetic field at the trap center [21,22] or by employing a time-orbiting potential trap [20]. These methods will result in a change of the trapping geometry.

The distance of the self-sufficient trap from the chip surface can be varied by changing the relative magnitude of the two vortex-loading pulses. The second, weaker pulse overwrites some of the field of the first pulse and provides the “bias field” necessary to produce a field zero above the chip surface. To move the self-sufficient trap further away from the surface it is thus necessary to reduce the magnitude of the second field loading pulse. In doing so the field resulting from the first pulse is made stronger, while the bias field resulting from the second pulse weakens. Figure 6 shows the distance of the self-sufficient trap from the superconducting surface and the atom number for different strengths of the second loading pulse. To measure the trap distance we make use of the mirrored cloud in Fig. 4. Since the magnetic field falls off as $1/r^2$ the depth and gradient are reduced for traps far away from the chip. The trap depth and gradient are also reduced in proximity to the chip surface, because the supercurrent cancels the magnetic field at the center of the square. However, the presence of the edges of the superconducting square in the images prevents us from making quantitative measurements of trap distances close to the chip.

To further demonstrate the flexibility of supercurrents produced by our method we demonstrate controlled splitting of the magnetic traps without the need to structure the underlying material. As numerically simulated in [17], applying a two-pulse sequence such as the one in Fig. 3(b) followed by an additional bias field, it is possible to produce a ring-shaped trap on top of a superconducting disk without the need to fabricate an on-chip ring structure. In analogy to the case of the

FIG. 6. (Color online) Distance of the self-sufficient trap from the superconducting square surface (black squares) and atom number (blue diamonds) vs strength of the second loading pulse. A stronger pulse produces a trap closer to the chip surface. The atom number far away from the chip is reduced due to a shallow trap depth.

FIG. 7. (Color online) (a) Magnetic trap geometries formed by two loading pulses $B_{\text{pulse}} = 600$ G and $B_{\text{pulse}} = -226$ G while holding the atoms in an external quadrupole gradient plus an additional bias field B_{bias} . (b) For low values of B_{bias} there exists one field zero above the square center. (c) Increasing the magnitude of B_{bias} brings the atoms closer to the chip surface and splits the initial, central trap into four. Due to the angle of the imaging two of the clouds are hidden behind the other two atom clouds.

disk, supercurrents loaded into a square by this pulse sequence should produce four distinct traps at the four corners of the square. Figure 7 shows absorption images of the atoms trapped by this field with an additional bias field B_{bias} . For low values of B_{bias} a single quadrupole trap is formed above the square center. Increasing the magnitude of the bias field cancels the

chip field closer to the surface and the initial, single quadrupole trap is split into four, analogous to a four-way beamsplitter. Due to the geometry of the imaging system, two of the split clouds are not visible in the figure. By tailoring different loading field sequences or even applying gradients to the superconductor it is possible to create a variety of different trap designs from a single, unpatterned square of type-II superconducting material.

IV. CONCLUSION

We have shown that by applying magnetic field pulses perpendicular to the surface of a type-II superconducting square it is possible to create a fully self-sufficient magnetic trap for ultracold atoms. We estimate the trap depth to be $10 \mu\text{K}$ and observe a magnetic trap lifetime due to Majorana spin flips of 1.5 s after an initial loss of atoms due to evaporation by the chip surface. Spin flip losses can potentially be overcome by dressing the atoms with a radio-frequency field to remove the zero of the magnetic field at the trap center [21,22] or by producing a time-orbiting potential trap [20]. However, doing so will result in a change of the trapping geometry. While the trap is loaded from an atomic cloud with a temperature of $200 \mu\text{K}$, evaporative cooling prior to loading the self-sufficient trap should result in a higher trapping efficiency. To demonstrate the versatility of writing supercurrent flows into type-II superconductors we show that using the same superconducting square, four distinct quadrupole traps can be formed at the square corners by a two pulse sequence and a bias field. By reducing the bias field the traps can be merged into a single quadrupole trap, resulting in a four-way splitter for the atoms. The flexible control over the trapping parameters after fabrication, and the low noise inherent in cryogenically cooled chips and supercurrents, give our system distinct advantages over traps generated by normal currents or permanent magnets.

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