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Complete architecture of integrated photonic circuits based on AND and NOT logic gates of exciton polaritons in semiconductor microcavities

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We present a complete photonic logic gate architecture in a compact solid-state system, making use of the nonlinear and spintronic properties of exciton polaritons in semiconductor microcavities. The dynamics of the system is modeled using the spinor Gross-Pitaevskii equations, and it is shown that the proposal fulfills all the necessary criteria for fully functioning information processing devices without the use of any external electric fields.

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I. INTRODUCTION

The construction of photonic devices typically relies on electro-optical effects to provide functionality as in recent modulator designs based on ring resonators,1 graphene,2 and exciton condensates.3 Fully photonic devices that do not require external electric fields are harder to come by due to the limited optical nonlinear response of typical materials. While on-chip diodes have been recently demonstrated,4 a complete photonic logic gate architecture has not yet been developed in a compact system, which would allow realization of optical microprocessors. Here we introduce such an architecture in a system based on the spintronic properties of exciton polaritons.5 This hybrid light-matter design is highly compact and offers the advantages expected of photonic circuits, including6 no distance-dependent loss or signal gradation, electrical isolation and immunity to electromagnetic interference, reduced error-checking overhead, and direct coupling to external light, useful for fiber-optic communication.

For a functional circuit architecture, five criteria must be fulfilled:7 (1) Universal Logic—Both AND- and NOT-type gates (or an equivalent set) are required; (2) Cascadability—The output of one gate must be able to drive the next; (3) Fan Out—It must be possible to split and duplicate signals; (4) Amplification—Loss must be fully compensated such that signals are maintained at the logic level; (5) Input-Output Isolation—The circuit must operate in one direction only, with no significant feedback effects from the output.

Beginning with the first criterion, one requires some kind of nonlinear element. In exciton-polariton systems—hybrid states of light confined in a microcavity and quantum well excitons5—this is provided from exciton-exciton interactions in the form of an effective Kerr-type nonlinearity. These nonlinear interactions have allowed the demonstration of optical modulators/transistors8–10 and amplifiers.11,12 Exciton polaritons also carry a spin,13 which can take values +1 or −1 when projected on the structure growth axis, and this has allowed the construction of spin switches.14,15 Unfortunately, none of the aforementioned devices has demonstrated the criterion of Cascadability or Fan Out and external optics would likely be necessary to do so, making for bulky and unscalable devices. Very recently a scheme of Cascadability and Fan Out was demonstrated,16 based on earlier work on the controlled shifting of hysteresis curves.17 However, the scheme was not completed with a Universal Logic. Indeed, as one tries to fulfill more criteria simultaneously, the task becomes exponentially more challenging.

On the theoretical side, a design for optical circuits was proposed in 2008,18 making use of the phenomenon of spin multistability19 in resonantly excited microcavities. Spin-polarized signals were carried by propagating spin domains along channels known as polariton neurons. This theoretical proposal solved the problems of Cascadability, Amplification, and Fan Out, since the spin-domain propagation continued without decay, despite the finite lifetime of exciton polaritons, which was particularly short at the time (3 ps). However, a Universal Set of logic gates was not realized: it was possible to construct AND-type or OR-type logic gates (but not both simultaneously), and it was not known how to construct a NOT-type logic gate. Both AND- and NOT-type gates are required for Universal Logic.

More recently, improvements in sample growth technology have increased the exciton-polariton lifetime by orders of magnitude,20 and the ballistic propagation of spin signals can proceed over hundreds of micrometers21 with minimal scattering. We make use of this development in combination with spin precession in one-dimensional (1D) exciton-polariton waveguides22 to couple multiple nodes in a network designed for the realization of optical circuits (see Fig. 1). By exciting each node in the network by a cw pump, the presence of polarization multistability allows each node to be switched between different spin states. These states are fully stable, where the optical pumping provides Amplification to fully compensate the still finite lifetime.

II. MODEL

To create the network of nodes we make use of the possibility to spatially pattern the potential $V(r)$ of exciton polaritons,23–25 as shown in Figs. 1(a) and 1(b). The polaritons will be free to move in the shaded area, allowing their transport between nodes (circular confined area) through 1D channels.

We model the dynamics of the polariton field with right circular polarization, using the spinor Gross-Pitaevskii (GP) equation,19,26

$$i\hbar \frac{\partial \Psi_+}{\partial t} = \left[ E_L P(k) + V(r) - \frac{i\hbar}{2\tau} + \alpha_1 |\Psi_+|^2 + \alpha_2 |\Psi_-|^2 \right] \Psi_+ + \Delta(r) \Psi_- + \bar{\alpha}_+ (r, \mathbf{r}),$$

where $\Psi_+$ and $\Psi_-$ are the spinor wave functions, $E_L P(k)$ is the linear photon energy, $V(r)$ is the potential energy due to the lattice, $\alpha_1$ and $\alpha_2$ are the nonlinear coefficients, $\Delta(r)$ is the detuning, and $\bar{\alpha}_+$ is the coupling to the external cavity. This equation describes the evolution of the polariton field under the influence of the external optical fields and the on-chip waveguide. The coupling term $\bar{\alpha}_+$ allows for the excitation of the polariton field by an external optical field, which is necessary for the implementation of logic gates.

The model is solved numerically using a finite difference method, allowing for the simulation of the polariton dynamics in the presence of various external optical fields. The results of the simulation are then used to design and optimize the photonic logic gates, ensuring that all five criteria are fulfilled.

The proposed photonic logic gate architecture is highly compact and fully optical, offers the advantages expected of photonic circuits, and can be implemented in a compact solid-state system, making use of the spintronic properties of exciton polaritons in semiconductor microcavities. The dynamics of the system is modeled using the spinor Gross-Pitaevskii equations, and it is shown that the proposal fulfills all the necessary criteria for fully functioning information processing devices without the use of any external electric fields.
the bistability at the nodes of the system. The dashed vertical line corresponds to the background intensity given by $|F_i|^2$. Points C and B correspond to the polariton density when a pulse $P_i$ is applied and after it has finished, respectively.

\[
\frac{i\hbar}{\tau} \frac{\partial \Psi}{\partial t} = \left[ E_{L,P}(k) + V(r) - \frac{i\hbar}{2\tau} + \alpha_1 |\Psi_-|^2 + \alpha_2 |\Psi_+|^2 \right] \Psi_- + \Delta(r) \Psi_+ + g_\gamma(r,t), \tag{1}
\]

where $\tau$ is the finite lifetime of polaritons, $\alpha_1$ and $\alpha_2$ characterize the nonlinear interactions, $g_\gamma(r,t)$ represents the right ($s = +$) and left ($s = -$) circularly polarized components of the optical excitation, and $E_{L,P}(k)$ represents the nonparabolic dispersion of the lowest polariton band (assuming near-resonant excitation higher energy states are not populated).

The polaritons are created with right ($\sigma^+$) and left ($\sigma^-$) circularly spin-polarized components in each node using a normal incident near a resonant Gaussian pump:

\[
g_\gamma(r,t) = \sum_{i=1}^{4} F_i \exp \left[ -\frac{(r - r_i)^2}{\ell^2} - \frac{i\epsilon_p t}{\hbar} \right] + P_i(r,t), \tag{2}
\]

where $r_i$ is the center of each node (see Fig. 1). In the channels, there is no optical pumping, such that exciton polaritons are free to propagate ballistically.

The potential $\Delta(r)$ appears along the channels, as a consequence of the energy splitting of the optical modes with polarization parallel and perpendicular to the channel length. This splitting results in a similar energy splitting of the exciton polaritons, inversely proportional to the width of the channel. Such potential will induce the precession of the polarization along the channels since it mixes $\Psi_+$ and $\Psi_-$ states, as can be appreciated in Eq. (1).

When the energy of the pump is chosen above $\sqrt{\hbar E_{L,P}(k)}$, the dependence of the polariton density $|\Psi_+|^2$ on the pump power $|\Psi_+|^2$ follows Fig. 1(c). Due to the presence of nonlinear interactions, the curve takes an S-shaped form, characteristic of bistability. For the pump power indicated by the vertical dashed line, the system can exist in one of two stable states marked by the points A and B.

Generally $|\alpha_1| \gg |\alpha_2|$; therefore there are only weak interactions between polaritons with opposite spin components, and one can expect a similar bistable response for each of the $\sigma^+$ and $\sigma^-$ components. With this spin degree of freedom, the system becomes multistable and one can write the input state using the circular polarization degree of the system $\rho = \frac{|\Psi_+|^2 - |\Psi_-|^2}{\rho^2}$ as follows.

The first term in Eq. (2) provides a Gaussian elliptically circularly polarized cw pump which creates polaritons $\sigma^+$ and $\sigma^-$ on the lower branch [around point A in Fig. 1(c)]. Sending a pulse $P_+$, we can increase the intensity of the excitation for the $\sigma^+$ polarization, and then the $\sigma^+$ state of polaritons is excited to the upper branch (point C). When the $P_+$ pulse is turned off, the $\sigma^+$ state of polaritons moves to point B. Meanwhile $\sigma^-$ polaritons remain around point A during the whole process. In this case, the final state is strongly $\sigma^+$ polarized.

Notice that the cw pump is slightly biased to $\sigma^+$ so as to favor one polarization in the case where two of them meet (we will use this to construct AND gates shortly). Initially the

FIG. 2. (Color online) Evolution of the circular polarization degree $\rho$ of the system at different times. In the first row we can see the system at the time $t_0 = 60$ ps, when the input pulse is sent writing the state of the first node. The pulse has a duration of $\tau = 18$ ps; therefore for the successive times shown the system is evolving without the pulse. The state reached at $t = 210$ ps is a stable state.
population of $\sigma^+$ polaritons will be slightly bigger than the $\sigma^-$ polaritons, but both will initialize in the lower bistable branch.

### III. UNIVERSAL LOGIC GATES

In Fig. 1(a) we show the proposed architecture for the NOT gate. The input signal is sent by directing the pulse over the first node (see top diagram in Fig. 2) as

$$\mathcal{P}_{\sigma}^N(r,t) = P_\sigma^N \exp \left[ -\frac{(r-r_1)^2}{l^2} - \frac{i\varepsilon_pt}{\hbar} - \frac{(t-t_0)^2}{\delta t^2} \right]. \quad (3)$$

Once the input node is initialized, the pulse $\mathcal{P}$ can be turned off. The polaritons in the initialized node increase their potential energy (blueshift effect) and are accelerated towards the channels where they travel ballistically between nodes. The potential $\Delta(r)$ causes a precession of the spin of polaritons traveling in the channel, resulting in a complete inversion of the spin for the polaritons arriving at the next node.

When an incoming flux of polaritons arrives at an unswitched node, the increase in the density of polaritons induces a “jump” to the upper branch of the bistable curve [point B Fig. 1(c)]. This results in a switch of the node with a polarization opposite to the preceding one. Notice that in this case the switching mechanism is different from the external pulse used to write the input state.

In other words, subsequent nodes switch with inverted spin polarization, as shown in Fig. 2. Each connection between nodes realizes a NOT logic gate with the input and output given by the truth table in the inset in Fig. 1(a). In this scheme there is complete Cascadability and Amplification.

To realize Universal Logic we also require an AND-type logic gate with the truth table shown in the inset to Fig. 1(b). Here we will use the potential profile shown in Fig. 1(b). The left ($r_1$) and right ($r_3$) nodes are the input and can again be set with the application of laser pulses as

$$\mathcal{P}_{\sigma}^A(r,t) = \left( P_{\sigma}^a \exp \left[ -\frac{(r-r_1)^2}{l^2} \right] + P_{\sigma}^b \exp \left[ -\frac{(r-r_3)^2}{l^2} \right] \right) \ast \exp \left( \frac{i\varepsilon_pt}{\hbar} - \frac{(t-t_0)^2}{\delta t^2} \right). \quad (4)$$
By changing the values of the amplitudes $P^a$ and $P^b$, the state of the input nodes can be written as desired; for instance, the state on the first column of Fig. 3 is achieved with $P^a = P^b = 0$ and $P^a = P^b = 0.5 \text{ meV mm}^{-1}$.

Notice that due to the Cascadability of the system, the input can also be set by connection to earlier parts of a larger circuit. The central node ($r_2$) can be considered as the output, the signal of which propagates to the lower node ($r_4$).

The time evolution of the AND gate for different inputs is shown in Fig. 3. The state of the central node is determined by the state of the two input nodes. Since the cw field is slightly elliptically polarized, the $\sigma^+$ polarization is favored, allowing the AND functionality. Note that the change to elliptical polarization does not inhibit NOT gate operation such that the whole circuit is excited with a cw uniform polarization. The typical switching time of the AND and NOT gates is of the order of 25–50 ps per node, corresponding to a single-gate repetition rate in the range of tens of gigahertz. This figure of merit could be further improved by decreasing the distance between nodes or using cavities with a smaller polariton effective mass.

For the calculations we used the following parameters: $L = 11 \mu \text{m}$, $\tau = 18 \text{ ps}$, $\epsilon_0/\alpha l = -0.22$, $l = 10 \mu \text{m}$, $\delta t = 18 \text{ ps}$, $t_0 = 60 \text{ ps}$, $F_- = 0.15 \text{ meV mm}^{-1}$, $F_+ = 0.17 \text{ meV mm}^{-1}$, $\epsilon_p = 0.5 \text{ meV}$ (energy with respect to the lowest energy of the polariton band), and $\Delta (r) = 0.09 \text{ meV}$, for $r$ inside the channel and $\Delta (r) = 0$, otherwise.

When tuning parameters, the appearance of bistability together with an inversion of the spin along the channels is crucial. For the first matter, the energy of the laser must be chosen above $\sqrt{3} \hbar / (2 \tau)$; while for the precession of the spin, it is fully determined by the value of the length of the channel and the polarization splitting $\Delta (r)$, which is defined by the width of the channel. A narrower channel would increase the rotation of the spin and in order to obtain exactly the opposite spin, the length of the channel would have to be shorter. Therefore the system allows some freedom about the dimensions of the system, as long as the inversion of the spin takes place between nodes.

In our proposal, it is also important that $\sigma_2$ is negative, such that interactions between opposite spin polarizations introduce a redshift of the energy. This prevents a given polarization switching to the high-intensity state if the opposite polarization has already been switched in the same node, allowing input-output isolation (there is suppression of feedback to earlier nodes in the circuit). This is important for allowing a binary logic, where the state of each node takes only one of two possible values.

The power consumption of the device is expected to be dominated by the cw background field required for Amplification of the signals. Polarization multistability was previously demonstrated with a power consumption of 50 W cm$^{-2}$, but it can be further improved by increasing the lifetime of polaritons since, for switching, the polariton density injected must give a blueshift due to polariton-polariton interactions, determined by $\epsilon_p = |\Psi_1|^2 |\alpha_1| \sim \sqrt{3} \hbar / (2 \tau)$. Here the polariton density scales with the pump power roughly as $|\Psi_1|^2 \sim F^2 \tau^2$. Thus the required pump power for bistability scales as $F^2 \sim 1 / (\alpha_1 \tau^3)$. In other words, an improvement of the polariton lifetime by a factor of 20 (as reported recently) can be expected to result in a power consumption an order of magnitude lower than the power consumptions in state-of-the-art photonic crystal cavities.

IV. CONCLUSIONS

We have presented a scheme for the realization of complete photonic integrated circuits in a single, compact solid-state system. Unlike previous schemes, we demonstrate the criteria required for fully functional devices including complete Universal Logic. By making use of spin multistability there is total logic-level Amplification of the states, completely compensating the finite lifetime of particles in the system. We expect these results to open a new horizon for the development of novel optical-based computers. A further interesting perspective is the ability to engineer polariton potentials optically, allowing one to dynamically update the circuit design. This could allow the completion of long circuits in small areas, giving a route for an optical device to compete with electronics in terms of device size, despite the comparably long wavelength of light. Since any transition between optical and electrical devices would be most likely to occur in stages, hybrid electro-optic systems are also of great interest. The presented scheme can be expected to be controlled by the application of electric fields which shift the polariton energy via the quantum confined Stark effect.

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