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Resistive switching memories in MoS$_2$ nanosphere assemblies

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A resistive switching memory device consisting of reduced graphene oxide and indium tin oxide as top/bottom two electrodes, separated by dielectric MoS$_2$ nanosphere assemblies as the active interlayer, was fabricated. This device exhibits the rewritable nonvolatile resistive switching with low SET/RESET voltage ($\sim$2 V), high ON/OFF resistance ratio ($\sim$10$^4$), and superior electrical bistability, introducing a potential application in data storage field. The resistance switching mechanism was analyzed in the assumptive model of the electron tunneling across the polarized potential barriers. © 2014 AIP Publishing LLC. [http://dx.doi.org/10.1063/1.4862755]

The resistance switching (RS) phenomenon basing on the electrically stimulated change of the resistance in a metal–insulator–metal (MIM) sandwich structure has recently attracted a great deal of attention due to potential application for nonvolatile random-access memory (NVRAM).$^{1-3}$ In the resistive memory cells, the resistance switches between a high resistance state (HRS) and a low resistance state (LRS) under the regulation of the formation and elimination of conductive pathways induced by the external electric field.$^4$ Developing nanoscale RS memory cells with high ON/OFF resistance ratio, fast switching speed, and low power consumption is one key step for achieving the application of ultrahigh density NVRAM.$^5$ So far, the RS characteristic has been observed in many semiconducting and insulating materials including binary transition metal oxides,$^6$ perovskite oxides,$^9$ chalcogenides,$^{12,13}$ sulfides,$^{14}$ amorphous silicon,$^{15}$ and organic materials.$^{16,17}$ Accordingly, various models have been suggested, including the metal-insulator phase transition,$^{11,18}$ the ferroelectric polarization,$^{19,20}$ and the conductive bridge constructed by the migration of localized metal atoms or defects.$^{8,21}$ and the RS mechanisms are still being debated.$^{1,2}$ Recently, molybdenum disulphide (MoS$_2$) has aroused increasing interests for various applications in electronic and optoelectronic devices, such as field-effect transistors (FET),$^{22,23}$ integrated circuits,$^{24}$ phototransistors,$^{25,26}$ and so on. Moreover, the different conductive properties like metal, insulator even superconductivity have been observed, respectively, in MoS$_2$,$^{27,28}$ although the natures of charge transport still remain elusive.$^{29}$ More recently, the MoS$_2$/graphene heterostructure memory devices have been constructed by using monolayer MoS$_2$ as the channel and graphene as electrodes in the FET geometry, and the gate voltage-modulated nature of electronic states in the two-dimensional (2D) ultrathin MoS$_2$ nanosheets has been demonstrated.$^{30,31}$ And Yin et al.$^{32}$ reported recently a sandwich-structured memory diode by using the mixture of 2D MoS$_2$ and graphene oxide (GO) nanosheets as the channel material, which showed that the MoS$_2$ nanosheets introduced in the mixed MoS$_2$-GO film might facilitate the oxygen migration of GO during device operation but the really active component was still GO. Herein, in the MoS$_2$ nanospheres-based diode, we found that the MoS$_2$ channel conductivity depends directly on the bias voltage, resulting significantly in the bipolar RS effect with low switching voltage ($\sim$2 V), high ON/OFF resistance ratio ($\sim$10$^4$), and superior electrical bistability. It is worth noting that the nanoparticle assembly system was proposed in 2009 by Kim et al.$^{33}$ to be promising for RS devices in the context of studying extensively the thin film-based RS devices; however, there are few following reports on the RS behavior in particle systems. In the present MoS$_2$ nanosphere assemblies, a model of electron tunneling across the polarization potential barriers modulated by the external electric field is proposed as one possible interpretation for the RS behavior. This work devotes to deepen understanding of the RS mechanism and to expand nonvolatile memory devices based on the MoS$_2$ nanosphere assemblies.

The diode involving MoS$_2$ nanospheres, reduced graphene oxide (rGO), and indium tin oxide (ITO) as the top and bottom electrodes was fabricated. The MoS$_2$ nanosphere assembly layer (around 200 nm) was deposited onto ITO glass by a hydrothermal route. Typically, 6 mg sodium molybdate (Na$_2$MoO$_4$-2H$_2$O) and 12 mg thioacetamide (C$_2$H$_2$NS) were dissolved in 40 ml deionized water to form a transparent mixture. This solution was transferred to a 100 ml Teflon-lined stainless steel autoclave. Then, a cleansed ITO glass was immersed in the above mixture with a leaning angle against the wall of the autoclave chamber and the autoclave was heated at 200°C for 20 h in an electric oven. As shown in the scanning electron microscopy (SEM) image of as-synthesized sample in Fig. 1(a), a mass of MoS$_2$ nanoparticles assemble in the ITO substrate and the amplified SEM image of several particles around the edge in the inset show that the MoS$_2$ nanoparticles are shaped like spheres with average diameter of 100 nm. The energy dispersive X-ray spectrum (EDX) shown in Fig. 1(b) indicates that the grown nanospheres are composed of characteristic Mo and S...
elements, whereas some other detected elements including Si, In, Ca, and O should come from the ITO glass. Fig. 1(c) presents the XRD pattern of sample, where the standard pattern of hexagonal MoS₂ phase (JCPDS card No. 37-1492) is also inset as a reference. Except two dominated diffraction peaks of 29.8 and 34.7° originated from ITO, the other detected peaks can be assigned to the (004), (104), (105), and (110) planes in the hexagonal MoS₂ phase. The indistinction of the (002) peak corresponding to the c-plane of layered MoS₂ is reasonable for the present MoS₂ nanospheres. Fig. 2(a) shows the X-ray photoelectron spectroscopy (XPS) survey spectrum of the as-synthesized MoS₂/ITO film, the characteristic peaks of chemically coordinated Mo and S elements are detected in addition to some peaks of inevitable C and O elements and In element from ITO glass. The amplified XPS spectra of Mo 3d and S 2p regions in Figs. 2(b) and 2(c) identify the strong Mo 3d₅/₂ and S 2p₃/₂ bands located, respectively, at 229.2 and 162.4 eV, indicating that Mo⁴⁺ and S²⁻ are the dominant oxidation states.

To fabricate a MIM device structure without the forming of short circuits, we employed an rGO sheet as the top electrode to cover on the pressed MoS₂ nanosphere assemblies. A great number of studies have demonstrated the good conductivity of high temperature annealing-reduced graphene oxide films, and the excellent performances of rGO electrodes used in various electronic devices, such as solar cells, FETs, and electronic storage, have also been reported recently. Herein, using, respectively, rGO and ITO as top and bottom electrodes, we constructed a MoS₂ nanospheres-based diode device, and the assembly procedure is schematically shown in Fig. S1. Briefly, the GO film was prepared first via spin-coating the GO aqueous solution on a SiO₂/Si substrate similar to the previous method. Then, the highly reduced GO film was obtained by high temperature (1000°C) annealing and was transferred onto the as-grown MoS₂/ITO by a normal transfer route. Finally, the adherent the poly(methyl methacrylate) (PMMA) on rGO was removed by using the ultraviolet (UV) irradiation and the developer of isopropyl alcohol (IPA):methyl isobutyl ketone (MIBK) basing on a process proposed by Liu et al.

Fig. 3(a) shows the current–voltage (I–V) characteristic of the as-fabricated rGO/MoS₂/ITO diode, and its schematic was illustrated in right inset. At low voltage, the device exhibits HRS. The current gradually increases with a negatively increased voltage (stage 1). The resistance retains the HRS until the voltage approaches the switching threshold of about −2.0 V, at which the current jumps abruptly from 4.2 × 10⁻⁵.
to the maximum limited current of 0.01 A in our semiconductor characterization system (Keithley 4200) (stage 2), indicating that the resistance switches from HRS to LRS. This SET operation from conductivity OFF to ON states represents a "writing" process in the memory device. The ON state with the high conductivity can remain in the subsequent negative sweep (stage 3) and reverse sweep of positive voltage less than 1.6 V (stage 4), indicating that the written data are of the non-volatile nature in the memory window of \( 2.0-1.6 \text{ eV} \) including zero voltage. Interestingly, once the positive sweep voltage reaches more than 1.6 V, the LRS can be switched back to the HRS (stage 5). This RESET operation from the conductivity ON to OFF states acts as the erasing function of storage data. After the RESET, the device remains in the OFF state in the subsequent sweep of positive voltage less than 6 V (stage 6) and can be reprogrammed to the ON state in the next sweep. The repeatable "SET-RESET" operation was further demonstrated by sweeping the voltage through multiple consecutive cycles in Fig. S2. The capacity of voltage regulating resistance state indicates the achievement of non-volatile memory with erasable and rewritable functions. In addition, we carried out the model-fitting for the experimental I–V characteristics of the device in HRS and LRS, respectively. In the HRS, the I–V data were fitted by the thermionic emission (TE) model \(^42\) with Eq. (S1) and the space-charge-limited current (SCLC) model \(^39\) with Eq. (S2) in Figs. S3(a) and S3(b), respectively. A more fitting linear relation was observed in the plot of \( \ln(I) \) vs. \( V^{1/2} \), indicating that the conduction mechanism probably arises from the TE model, and the charge injection and accumulation in dielectric nanospheres may be dominant in the OFF state. And, the I–V characteristic in the LRS could be fitted well by an ohmic conduction model \(^43\) with Eq. (S3) in Fig. S3(c), indicating definitely that the good ohmic conductive channels are established in the ON state. A constant voltage of 1.0 V was applied to investigate the retention property of the memory cell at room temperature, as shown in Fig. S3(b). Both the ON and OFF states were stable during the test period of \( 5.5 \times 10^3 \) s, and the high ON/OFF resistance ratio of \( \sim 10^4 \) did not decline obviously. The strong retention ability with high ON/OFF ratio between the HRS and LRS is crucial both to preserve persistently the stored information and to restrict the misreading probability in the practical application of memory devices.

Note that the MoS\(_2\) film-based device does not exhibit significantly the RS phenomenon, as shown in Fig. S4, which is in agreement with some recently reported findings. \(^31,32\) Thus, we consider that the unique shape of nanospheres may play a crucial role in the carrier-transport process, and a general model of electron tunneling across the polarization potential barriers \(^44\) in dielectric particle networks was proposed accordingly to understand the electrically controlled RS effect observed in our experiment. Fig. 4 shows the schematic, illustrating the electric-field-modulated change of conductive pathway. For the nanospheres assembling network film, the physical contact between the adjacent spheres results in massive grain boundaries and high junction

FIG. 3. (a) Typical I-V curve and schematic illustration (inset) of the rGO/MoS\(_2\)/ITO memory device. (b) Retention tests of the ON and OFF states with the ON/OFF ratio of around \( 10^5 \) at -1 V.

FIG. 4. Model schematic of the electron tunneling across the junction barriers modulated by electric-field-induced polarization with SET and RESET processes.
barriers, which dominate the electron transport and make the whole diode be in the initial HRS, as shown in Fig. 4(a). Setting the applied electric field to a relatively low value, the charge will gradually inject and accumulate in individual sphere cells following the above mentioned TE model due to the difficulty of electron tunneling in HRS, thus resulting in the formation of polarized domains. Moreover, the increased charge density in spheres reduces the junction-barrier height, allowing easier electron tunneling and transportation, i.e., the conductive pathway is formed along the boundaries of the spheres and the diode is set into the LRS, as illustrated in Fig. 4(b). More importantly, because of the existence of bound charges and internal polarization fields, the conductive pathway can always survive until a reversely applied electric field is high enough to rupture the polarized domains, as shown in Fig. 4(c). Once the polarization charges accumulated near surface of spheres are relaxed, the junction barriers between spheres will resume the natural height, hindering the electron tunneling and transportation. At this point, the conductive pathway is destroyed, as shown in Fig. 4(d), and the diode is reset into HRS. But, it is worth noting that the electric-field-induced polarized domains in tunnel barriers have a certain direction, which is contrary to that of the applied first electric field. Since the vectorial polarization is a collective phenomenon like magnetization, the polarization fields, and bound charges induced by the applied first electric field in dielectric nanospheres cannot indeed be released in full in one operation cycle, as shown in Fig. 4(d). Thus, the reverse RESET voltage can reset the device to the HRS rather than to the original ground state. Fig. S5(a) shows the amplified I−V curve of Fig. 3(a), and we can identify the difference between the initial and the reset OFF states, which is highlighted by a red circle. This indicates that the reset HRS is not the ground state and it may remain still some formerly polarized domains or Schottky barriers, which make the reset device less easy to occur the polarization reversal and resistance-state switching during the last sweep of positive voltage less than 6 V. However, if setting first positive voltage, the polarized domains in opposite direction will grow and remain in the whole operation cycle, then the converse “SET–RESET” process can be manipulated, as shown in Fig. S5(b). Thus, the vectorial polarization induced by the SET field affects the conduction behavior of device in the whole sweeping cycle including in the HRS after RESET operation. In a word, the electric-field-induced polarized domains with varying junction barriers in the nanosphere networks may have an important effect on the formation and rupture of conductive pathway in the bipolar RS behavior. Indeed, it should be noted that the nanoparticles/nanospheres as building blocks have shown the promising applicability for many electronic nanodevices, but the fantastic electron-transport property of their assemblies is still an open issue and needs more researches. The present interpretation of observed electrical behavior of nanosphere assemblies as of memristor is not conclusive and should be explored more accurately by using more profound theories in the future.

In conclusion, we have demonstrated that the MoS2 nanosphere assemblies fabricated by a simple hydrothermal method can be employed as the active layer for the nonvolatile memory device. The device exhibits the sustainable conductivity bistability (HRS and LRS) and the typical bipolar RS with low switching voltage (∼2 V) and high ON/OFF resistance ratio (∼105). A possible model of the electron tunneling across the junction barriers modulated by electric-field-induced polarization was proposed to explain the observed RS behavior, indicating that the unusual electrical properties in the nanosphere or nanoparticle assemblies can expand further the application of relevant nanodevices.

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46 See supplementary material at http://dx.doi.org/10.1063/1.4862755 for the fabrication procedure, the SEM image, and the original data and the model-fitting data of I-V measurement of the rGO/MoS2 nanospheres/ITO memory device.