

1 **Effects of high-temperature thermal annealing on GeSn thin-**
2 **film material and photodetector operating at 2 μm**

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2 **Highlights**

- 3 1. Ordered nanopatterns are formed on the surface of GeSn from high-temperature
4 annealing ($\sim 700^\circ\text{C}$).
5 2. Structural and optical properties of GeSn films before and after rapid thermal
6 annealing are reported.
7 3. The decrease of the dark current with an enhancement of the I_p/I_d ratio (on-off ratio)
8 indicates an improvement of the photodetector detectivity due to the annealing
9 process at $\sim 700^\circ\text{C}$.
10 4. An increased photocurrent ($>200\%$) is observed for the GeSn photodetector at an
11 annealing temperature of 550°C at $2\text{-}\mu\text{m}$ wavelength.
12

13 **Abstract**

14 Here, we explore the thermal stability of GeSn epilayers with varying Sn
15 contents (3-10%) at an annealing temperature ranging from 300 to 750°C . It is
16 found that ordered nanopatterns are formed on the surface of GeSn with Sn
17 content of 8% without excessive Sn precipitation after thermal annealing at
18 700°C . Despite being annealed at high temperatures, the GeSn maintains its
19 crystal structure, which is confirmed by the X-ray Diffraction (XRD), Raman
20 spectrum, and secondary-ion mass spectrometry (SIMS). The corresponding
21 photocurrents of the photodetectors at the wavelength of $2\ \mu\text{m}$ also indicate the
22 crystal quality of the GeSn alloys does not deteriorate significantly after high-
23 temperature annealing ($675\text{-}700^\circ\text{C}$). Meanwhile, the decrease of dark current
24 with the enhancement of I_p/I_d ratio (on-off ratio) indicates the improvement of
25 detectivity of the photodetector due to the annealing process. Furthermore, the
26 annealing temperature is optimized to 550°C to achieve 200% enhancement of
27 photocurrents of the GeSn photodetectors operated at $2\ \mu\text{m}$.
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29 Keywords: GeSn alloy, annealing, structural property, nanopattern, infrared
30 photodetector
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1 **1. Introduction**

2 Germanium-tin (GeSn) is a promising material for extending the
3 applications of the current silicon-based electronic device technologies due to its
4 excellent carrier mobility and bandgap tunability [1-3]. However, the low
5 thermoequilibrium solubility of Sn in Ge (less than 1% at Ge-Sn eutectic of
6 231°C) makes the introduction of high Sn contents in a Ge matrix difficult due
7 to Sn segregation or precipitation. Recently, single-crystal GeSn layers with high
8 Sn contents have been realized by non-equilibrium growth techniques such as
9 chemical vapor deposition (CVD) [4-6], molecular beam epitaxy (MBE) [7, 8],
10 and sputtering epitaxy [9]. However, the low growth temperature of these
11 methods for avoiding Sn segregation is the major obstacle to obtain device-grade
12 GeSn materials due to the introduction of point defects [10], which are
13 detrimental to its electronic and optoelectronic applications.

14 Thermal annealing, the widely used technique for relaxing strain and
15 improving crystal quality, has been utilized to improve the GeSn film quality
16 [11-13]. The thermal annealing process does not only decrease the defect density
17 and improve the optical quality [14] but could also increase the portion of
18 substitutional Sn atoms in the GeSn layer [15]. Contrary to that of SiGe system,
19 the crystallinity of high-Sn content GeSn always deteriorates during the thermal
20 annealing process. Numerous efforts have been attempted to clarify the
21 relationship between the Sn segregation and strain relaxation and/or misfit
22 dislocation [12, 16-19]. Many reports have suggested that the critical annealing
23 temperature should be less than 600°C to avoid the Sn segregation after the strain
24 relaxation [12, 14, 20].

25 The 2 μm communication band has been suggested as a promising solution
26 for the projected ‘capacity crunch’ of conventional single-mode fibers (SMFs).
27 GeSn alloy offers an alternative approach from III-V materials to extend the
28 detection range of Ge towards 2 μm . Recently, GeSn photodiodes operated at 2
29 μm have been demonstrated on different platforms (Si, Ge, and Si-on-insulator)
30 [3, 21, 22]. However, the performance of the GeSn photodiodes at 2 μm is
31 relatively low compared to the III-V photodiode, mainly due to the high-density
32 defect generated from the low-temperature growth process. Thus, similar to other
33 materials the post thermal annealing could be a solution to decrease the defect
34 density of GeSn and improve its photodetector performance at 2 μm . Yet, to our
35 best knowledge, there is no previous work that reports the effects of thermal
36 annealing on the performance of a single-crystal GeSn photodetector.

37 In this paper, GeSn layers with varying Sn contents that range from 3 to 10%
38 are investigated. Rapid thermal annealing (RTA) with temperatures ranging from
39 300 to 750°C is carried out. Contrary to the previous research, we observed that
40 highly ordered nanopatterns are formed on the surface of GeSn with Sn content
41 of 8% after strain relaxation at an annealing temperature of 700°C. No usual
42 excessive Sn precipitation was observed. Despite the use of high annealing
43 temperature, the GeSn structure did not severely damage, as confirmed by the
44 XRD, micro-Raman spectroscopy. Thereafter, the effect of the annealing process

1 on the performance of the photodetector made from the GeSn film will also be
2 presented. We demonstrated that an optimized annealing temperature
3 significantly enhances the photocurrent of the GeSn photodetector (>200%) at
4 the wavelength of 2 μm .

5 6 **2. Experimental**

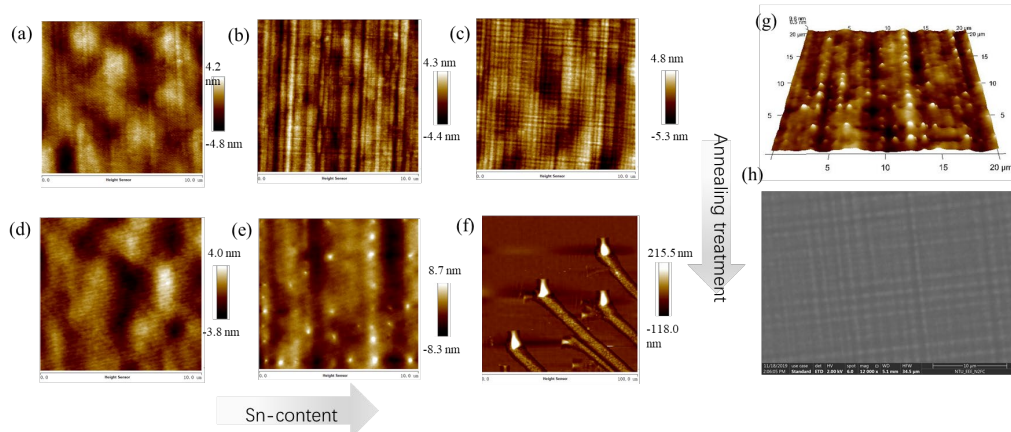
7 GeSn films with Sn contents ranging from 3.3 to 10.0% were grown on
8 strain relaxed Ge buffer layer with a thickness of 850 nm on 6" Si (100) substrates
9 in a reduced pressure chemical vapor deposition (RPCVD) chamber (ASM
10 E2000 reactor). The growth temperature ranges from 290 to 325°C and the
11 growth duration was fixed at 10 mins. The thicknesses of the GeSn films grown
12 with Sn content of 3.3, 8, and 10% are 180, 195, and 170 nm, respectively, as
13 confirmed by XRD and secondary ion mass spectrometry (SIMS). Subsequently,
14 the GeSn wafers were cut into smaller pieces (1.5 cm \times 1.5 cm) and annealed in
15 an RTA system (AnnealSys AS-One) at various temperatures ranging from 350
16 to 750°C in the nitrogen ambient. For the RTA process, a thermocouple was used
17 to monitor the temperature in real-time. The annealing time is 1 min unless
18 otherwise mentioned.

19 The structural and strain evolution of annealed GeSn films were analyzed
20 by XRD using $\text{CuK}\alpha 1$ ($\lambda_{\text{XRD}} = 1.5406 \text{ \AA}$, Panalytical X' Pert) radiation, including
21 the high-resolution x-ray reciprocal space mapping (RSM) and transmission
22 electron microscopy (TEM, FEI Tecnai F20). The surface morphology was
23 characterized by atomic force microscopy using the tapping mode (AFM, Veeco,
24 multimode V) and field emission scanning electron microscopy (FESEM, FEI-
25 Apreo-S). Raman (UHT S300 & WITEC), secondary ion mass spectrometry
26 (SIMS, EAG Laboratories in the USA) and SEM-energy dispersive X-ray
27 spectroscopy (SEM-EDS, JED 2300) were also used to analyze the chemical
28 composition. For the Raman analysis, the integration time is 20 s and the spot
29 diameter is 1 μm with a 100 \times objective lens. For the photo-electrical
30 characterizations of the GeSn metal–semiconductor–metal (MSM)
31 photodetectors, the samples were fabricated with conventional lithographic or
32 electron-beam lithographic processes. 100 nm Ti electrode was deposited on
33 GeSn/Ge/Si to form Schottky contact by electron-beam deposition. Photoelectric
34 characteristics were performed using SMU (Keithley 2450) with a 2 μm fiber
35 laser (Thorlabs-FPL2000) on the Probe Station.

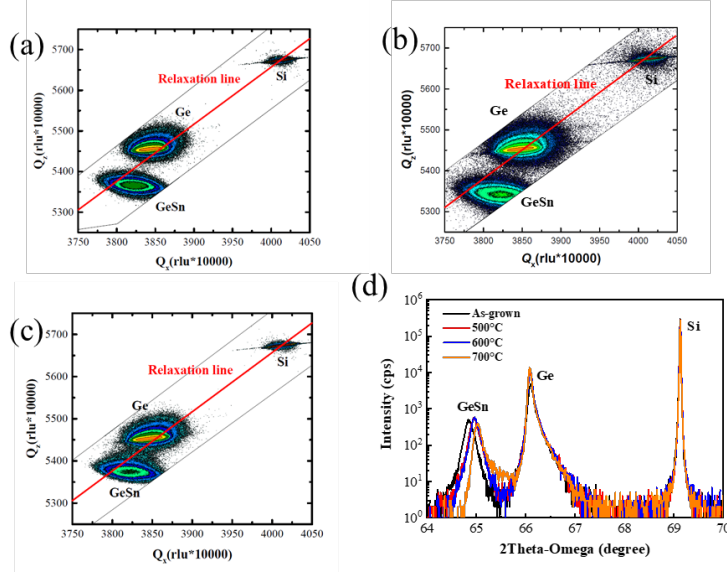
36 37 **3. Results and Discussion**

38 In Figure 1, AFM images were acquired on the GeSn samples with various
39 Sn contents (3.3, 8, and 10%) before and after RTA annealing. The crosshatch
40 patterns (CHPs) are observed on as-grown GeSn with two sets of perpendicular
41 features aligned along two [110]-equivalent directions. Its appearance intensifies
42 and becomes more pronounced with increasing Sn content (Figure 1(a)-(c)).

1 Furthermore, the spacing of the CHP decreases from 0.8 to 0.25 μm when the Sn
 2 content increases from 3.3 to 10%, respectively. Although the 3.3% Sn content
 3 sample was annealed at a temperature as high as 700°C, no noticeable change on
 4 the surfaces are observed. On the other hand, micro-trenches appear on the 10%
 5 Sn content sample annealed at 400°C (Figure S1). It is likely due to the self-
 6 running of the Sn-droplet caused by the Sn-segregation [19, 25, 26]. Some
 7 immobile Sn-rich droplets appear on the surface of the 8% Sn content samples
 8 after annealing at 700°C. However, most areas are free of Sn segregation (see the
 9 microscope pictures in Figure S2 and EDS analysis in Figure S3). After the high-
 10 temperature annealing process, highly ordered nanopatterns are formed on the
 11 hilltop of the crosshatch (Figure 1(e)). The height modulation increases from 8.6
 12 to 16.6 nm (Figures 1(b) and (e)), indicating that surface reconstruction occurs
 13 during the high-temperature annealing. Ordered nanopatterns on the annealed 8%
 14 Sn-content GeSn were observed by 3D-AFM and SEM as shown in Figure 1(g)-
 15 (h) and Figure S4. The surface reconstruction from annealing is frequently
 16 observed at the temperature $>650^\circ\text{C}$ as well, as shown in Figure S2.



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 18 Figure 1. Surface morphology of different Sn-content films before and after
 19 annealing treatment. (a) and (d) 3.3% Sn as-grown sample and annealed at 700°C;
 20 (b) and (e) 8 % Sn as-grown sample and annealed at 700°C; (c) and (f) 10% Sn
 21 as-grown sample and annealed at 400°C; (g) and (h) 3D-AFM and SEM images
 22 of the ordered nanopatterns on the annealed 8% Sn-content GeSn sample.



1
2 Figure 2. XRD-2DRSM around (224) reciprocal lattice point of GeSn epilayers:
3 (a) and (c) 8 % Sn as-grown and annealing samples(700°C); (b) 10 % Sn as-
4 grown samples; (d) (004) ω -2 Θ scans of the 8% Sn samples before and annealing
5 at 500, 600 and 700°C.

6
7 To further analyze the Sn incorporation and residual strain of the samples
8 before and after annealing, high-resolution X-ray RSM around the (224)
9 diffractions was performed (Figure 2(a)-(c) and Figure S5). Subsequently, Sn
10 content is estimated according to the procedure developed in Supplementary
11 Material. The macroscopic degree of strain relaxation R (%) in all GeSn layers
12 are calculated using the following equation:

$$R(\%) = \frac{a_{GeSn}^{//} - a_{Ge}^{//}}{a_{GeSn} - a_{Ge}^{//}} \quad (1)$$

13
14
15 where $a_{GeSn}^{//}$ and $a_{Ge}^{//}$ are the in-plane lattice parameters of the GeSn and the Ge
16 layer, respectively, and a_{GeSn} is the relaxed lattice parameter of the GeSn layer.
17 The estimated degree of strain relaxation of the as-grown GeSn with Sn contents
18 of 3.3, 8.0, and 10% is 20, 52, and 47%, respectively. GeSn with higher Sn
19 content has a larger lattice constant and relaxation [27]. The thickness beyond
20 which the larger lattice constant layer cannot accommodate the strain is referred
21 to as the critical thickness (h_c). The People and Bean (P-B) model, which
22 compares the elastic energy stored by the stressed layer and the energy for
23 dislocation nucleation, has been widely used to predict the critical thickness in
24 the SiGe/Si and GeSn/Ge system[28],

$$h_c \approx \left(\frac{1-\nu}{1+\nu}\right) \left(\frac{1}{16\pi}\right) \left[\frac{b^2}{a_{GeSn}}\right] \left[\left(\frac{1}{f^2}\right) \ln\left(\frac{h_c}{b}\right)\right], \quad (2)$$

$$f = \frac{a_{GeSn} - a_{Ge}}{a_{Ge}}, \quad (3)$$

where ν is the Poisson ratio, b is the length of the Burgers vector. Based on the above equations, the calculated h_c for the 3.3, 8, and 10% Sn-content sample is around 430, 55, and 37 nm. This explains the 3.3% Sn sample has a small strain relaxation with a thickness of 180 nm, while for the high Sn contents samples (8% and 10% Sn) with a similar thickness, more than 50% strain was relaxed. For the 8% Sn sample after 700°C annealing (Figure 2(c)), we found that the decrease in the Sn-content is negligible ($\sim 0.14\%$). However, the compressive strain changes from 0.45 to 0.30% with the strain relaxation increasing from 52 to 69%. Figure 2(d) shows the (004) ω - 2θ scans of the 8% Sn sample after annealing at the temperatures of 500, 600, and 700°C. It shows the right shift of the GeSn peak as the annealing temperature increases, indicating the increase of the strain relaxation.

Based on the above strain analysis, we can explain the occurrences of the CHPs and the ordered nano-patterns on the high Sn-content samples (8 and 10%). The CHP is a common feature for heteroepitaxial systems and has been widely observed in SiGe/Si [29], GaAs/Si [30], and InGaAs/GaAs [31] heteroepitaxial layers. For the growth of GeSn layers, the relaxation process starts with the formation of defects such as misfit dislocations (MDs) on the surface when the thickness beyond the critical thickness. In the heteroepitaxy on (100)-oriented substrates, the strain relaxation is generally completed with the nucleation of MDs at the hetero-interface. In this case, the Burger vectors form a 60° with the $\langle 110 \rangle$ direction (Figure 3(a)). These dislocations are glided along the inclined $\{111\}$ planes. As such, the effect induces a strain inhomogeneity at the free surface along the $\langle 110 \rangle$ direction which alters the binding energy of the atoms. According to the linear elastic theory, the binding energy varies linearly with external strain ε_{ext} and can be written as [32, 33],

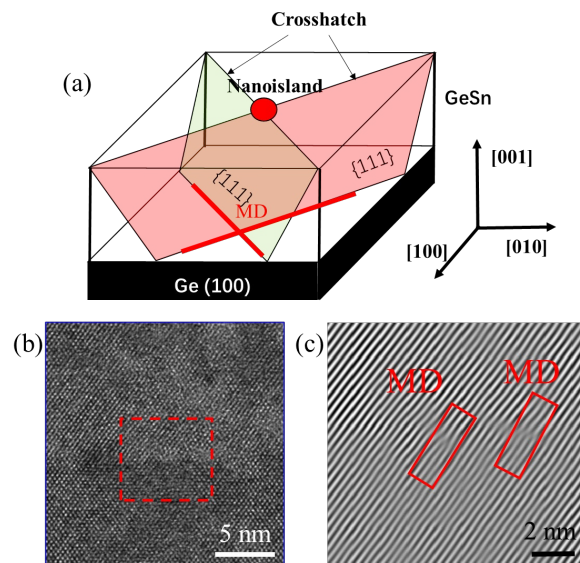
$$E_{ad} = E_{ad0} + A\sigma\varepsilon_{ext} \quad (4)$$

where E_{ad0} is the binding energy on an unstrained surface, A is the surface area, and σ is the surface stress tensor induced by the adatom. Thus, this modulation strain creates an inhomogeneity atom binding energy on the film surface. As a result, it induces the oriented crosshatch pattern at the film surface (Figure 3(a)).

The dislocation formations at the GeSn/Ge interface were studied by HRTEM. As shown in Figure 3(b) and (c), 60° mixed dislocations are formed near the interface, as shown from the HRTEM image viewed along $[110]$ direction. The corresponding inverse fast Fourier transform (FFT) images are obtained in Fig. 3 (c), confirming the 60° dislocations located at the (111) plane. Due to the low activation energy, 60° dislocations are dominant for the low-temperature ($< 400^\circ\text{C}$) epitaxial GeSn layer on Ge [4]. These two MD-induced

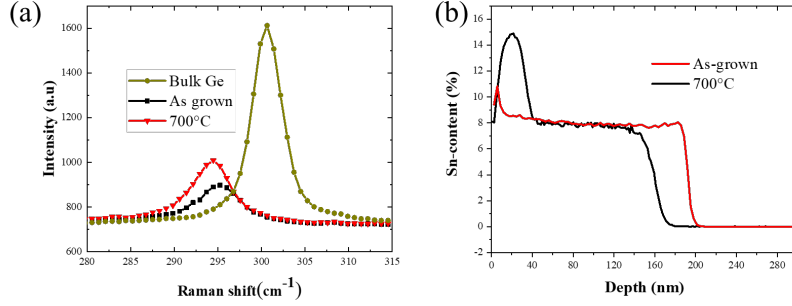
1 arrayed lines along the two orthogonal $[110]$ -equivalent directions indicate
 2 heavily relaxed strain points when the two vertical lines intersect (Figure 3(a),
 3 red dot). At this intersection, the strain relaxation is largest with the highest atom
 4 binding energy. Thus, during the high-temperature annealing process for GeSn
 5 with the 8% Sn content, the atoms on the film surface tend to migrate to this point
 6 to form the ordered nanopatterns, especially when the strain is further relaxed by
 7 the thermal effect [18].

8 The experimental observation of the surface reconstruction (ordered
 9 nanopattern) is different from previous works of GeSn films that serious Sn
 10 segregation occurs when strain relaxation happens during the annealing process
 11 [11,13,15,17]. We also note that some similar reports accounted for the ordered
 12 nano-islands or quantum dots (QDs) grown on the surface of heteroepitaxial
 13 films. Pandya *et al.* had demonstrated the self-organized, nanoscale 3D-island
 14 formation in perovskite thin films due to modulated strain fields [32].
 15 Furthermore, Leon *et al.* and Welsch *et al.* have reported the growth of spatial
 16 ordering of InAs QDs strain-relaxed $\text{In}_x\text{Ga}_{1-x}\text{As}/\text{GaAs}$ layers by utilizing the
 17 misfit dislocations [34, 35]. In our work, we only observe the ordered
 18 nanopatterns for GeSn with a Sn content of 8%. Typically, high Sn (10%) content
 19 GeSn is prone to Sn precipitation when annealed at a temperature higher than
 20 400°C , while for the low-Sn content (3.3%), the low lattice-mismatch from Ge
 21 makes the modulated strain fields difficult to form. On other hand, our GeSn
 22 sample is relatively thin (<200 nm) compared to GeSn layers of most previous
 23 annealing works. Thick GeSn samples are easy to segregate Sn as they have high
 24 dislocation density due to the strain relaxation during the growth. Thus, the Sn
 25 content and thickness of the film should be optimized to contain the ordered
 26 modulation strain and achieve the ordered pattern on GeSn from annealing. This
 27 observation of the GeSn surface reconstruction not only shows a new understand
 28 of the GeSn material characteristics during annealing but also provides an
 29 approach to realize self-patterned 3D nanostructures/QDs for further
 30 optoelectronic applications.



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1 Figure 3 (a) Schematic of the formation mechanism of the crosshatch and ordered
 2 nano-island due to the misfit dislocations. (b) The HRTEM image of the
 3 $\text{Ge}_{0.92}\text{Sn}_{0.08}/\text{Ge}$ interface with Sn-content of 10% along [110] direction. Inside
 4 shows the FFT pattern; (c) inverse FFT images were obtained in FFT, showing
 5 the extra (111) plane.



6

7 Figure 4. (a) and (b) Raman and SIMS curve of the $\text{Ge}_{0.92}\text{Sn}_{0.08}$ layer before and
 8 after annealing at 700°C.

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10 Micro-Raman spectroscopy with a 532 nm wavelength laser source was
 11 performed on the GeSn layers (Figure 4(a)). For the measurement, the input
 12 power was set low to avoid the heating effect from the laser source. The spectral
 13 of the bulk Ge also was measured under the same power condition. It is important
 14 to note that the corresponding penetration depth of a 532 nm laser is around 20
 15 nm in Ge and GeSn. Thus, only the region near the surface of the GeSn layer was
 16 measured. The Raman-strain relationship is a function of material composition
 17 and strain configuration as follows:

$$18 \quad \Delta\omega = \omega_{\text{Ge}} - \omega_{\text{GeSn}} = -ax - b\varepsilon \quad (5)$$

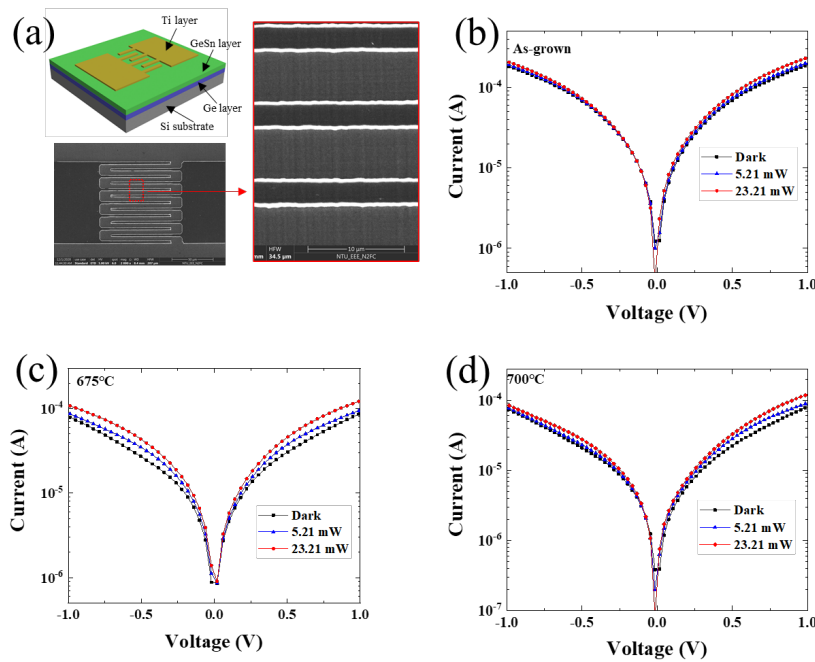
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20 Here, a is -88 cm^{-1} and b is $+521 \text{ cm}^{-1}$ according to Ref. [36] and $\Delta\omega$
 21 Raman spectral shift variation from 300.4 cm^{-1} (bulk Ge). The as-grown and
 22 annealed (700°C) GeSn with 8% Sn content displays the Ge-Ge peaks at 295.3
 23 and 294.5 cm^{-1} , respectively. By substituting the compressive strain of -0.45 and
 24 -0.30% estimated from the above XRD-RSM analysis, the Sn-content of the top
 25 surface layer of the GeSn sample was obtained to be 8.5% before and after
 26 annealing. Thus, the Raman results reasonably match that of the above XRD
 27 analysis, indicating the crystallinity of the surface layer. Besides, it is observed
 28 the intensity of the Ge-Ge peaks increases obviously after annealing. It might
 29 imply that the crystallinity of the GeSn has been enhanced.

30

31 SIMS was used to determine the Sn content changes of the GeSn/Ge before
 32 and after annealing at 700°C for the 8% Sn-content sample (Figure 4(b)). The
 33 measured area was $\sim 100 \mu\text{m}^2$ which covers hundreds of crosshatch patterns. As
 34 the height of the crosshatch/nanoisland is $\sim 10 \text{ nm}$ and the analysis depth is ~ 300
 35 nm, the measurement of the element content will mostly reflect the information
 36 of the film. For the as-grown GeSn, the entire GeSn film has a Sn-content around
 8%, which is consistent with the above XRD and Raman analysis. However, for

1 the annealed GeSn sample, the Sn content decreases at the GeSn/Ge interface
 2 while the Sn content increases at the surface. The total thickness of the GeSn
 3 layer decreases from 195 nm to 160 nm after the annealing. The Sn-content
 4 decreases at the GeSn/Ge interface might be due to the large lattice-mismatch
 5 strain near the interface, which promotes Sn expulsion from the lattice sites. The
 6 slope of Sn-content near the interface is less than that of the as-grown GeSn,
 7 indicating the tendency to eliminate interfacial strain. On the other hand, the
 8 SIMS results show that the Sn-content at the surface is as high as 14% which is
 9 higher than those obtained from the Raman spectroscopy and XRD analysis. This
 10 is because XRD and Raman only reflect the substitutional Sn concentration in
 11 the crystalline Ge. SIMS, on the other hand, includes all the Sn atoms from the
 12 GeSn layer.



13
 14 Figure 5 (a) 3d schematic and SEM image of finger-type MSM GeSn device on
 15 Ge/Si. (b)-(d) Current-voltage characteristics of the fabricated MSM
 16 photodetectors on the as-grown and annealed GeSn at 675 and 700°C measured
 17 in dark and under the illumination of 2 μm wavelength with the of 5.21 and
 18 23.21 mW.

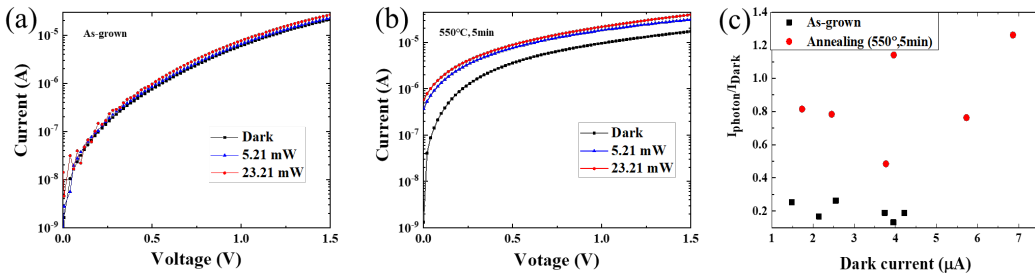
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 20 To the best of our knowledge, there is no previous work that reports on the
 21 effects of thermal annealing on the performance of a single crystal GeSn
 22 photodetector . The photocurrent of the GeSn MSM photodetector was measured
 23 at 2 μm wavelength with the optical power modulated between 0 to 23.21 mW.
 24 A single-mode fiber with a diameter of ~10 μm is coupled from the laser, which
 25 is less than the photodetector mesa areas (100*100 μm²). For the MSM
 26 fabrication, the GeSn epilayers (as-grown, annealed GeSn at 675 and 700°C) on
 27 Ge/Si substrate were firstly immersed in the buffered oxide etchant (BOE) to
 28 remove the Sn or Ge oxide layer. A ~1.5 nm-thick Al₂O₃ layer was then deposited

1 on GeSn epilayers by atomic layer deposition (ALD) at 250°C to alleviate the
2 fermi-level pinning [37]. Interdigitated electrode MSM structures were
3 demonstrated after a ~100 nm Ti deposition on the GeSn epilayers on Ge/Si
4 substrate followed by a lift-off process. The active area is composed of 3 μm
5 interdigitated fingers with 5 μm spacings. The 3D schematic and SEM image of
6 the finger-type MSM GeSn device are shown in Figure 5(a). The nanopattern GeSn
7 surface achieved from annealing could be observed on the active area of the device by
8 SEM. Figure 5(b)-(d) shows the typical current-voltage (I-V) characteristic of the
9 photodetectors before and after the annealing process. It shows that at 2 μm the
10 photocurrent increases when the power intensity increases. The annealing
11 samples (Figure 5(c) and (d)) have a better photon detection performance than
12 the as-grown sample (Figure 5(b)) as indicated by the larger photocurrent to dark
13 current contrast ratio (I_p/I_d ratio) of annealed GeSn photodetectors. At 1V, the
14 dark current of the annealed GeSn photodetectors is suppressed by 62.8 and 66.1%
15 compared to that of the as-grown GeSn photodetector. Furthermore, the photo
16 current at 1 V of the as-grown GeSn, 675 and 700°C annealed GeSn
17 photodetectors are 42.9, 35.8, and 39.8 μA, respectively, with the incident power
18 of 23.21 mW. These photo current values are consistent with our recent study on
19 the flexible GeSn MSM, which has a photon current of 43.6 μA at 2V under the
20 same laser power [38]. These results indicate despite subjecting the samples to
21 high annealing temperatures, the crystal quality of the GeSn has not significantly
22 deteriorated.

23 Due to the supersaturated vacancy from low-temperature growth, the
24 unintentionally GeSn samples have a p-type background doping. For GeSn, the
25 typical background doping concentrations are around 10^{17} cm⁻³[38]. For the
26 metal/p-Ge contacts, the hole dark current is consistently high due to the Fermi
27 level pinning at ~0.1 eV near the valence band [37]. Though an ultrathin Al₂O₃
28 dielectric layer is used to alleviate the Fermi-level pinning, the dark current of
29 the GeSn MSM photodiode is relatively high. Additionally, the device with the
30 large junction area (6.0×10^{-4} cm²) also aggravates the large dark current. Here,
31 the decrease of the dark current by the high-temperature annealing process might
32 be due to the slightly increased Schottky barrier caused by the elevation of the
33 Fermi level, resulting in the decrease of the hole concentration by the annealing
34 process. Further researches are required to evaluate the changes of the ideality
35 factor, Schottky barrier, and carrier concentration after annealing. A recent study
36 on the amorphous GeSn thin-film also demonstrated a decrease of dark current
37 by a factor of two (from 1 to 0.5 μA) on the MSM photodetector after annealing.
38 It suggests the annealed sample has a lower defect density than that of as-grown
39 samples based on the observation of a decrease of the ideality factor [39].

40 For the photon current at 2 μm, only the GeSn layer contributes to it as the
41 light absorption by Ge and Si is negligible at 2 μm due to their wider bandgap

1 (1.1 and 0.8 eV, respectively). The absorption coefficient (α) of Ge at 2 μm is ~ 1
 2 cm^{-1} , while the strain-free 8% Sn-content GeSn is $\sim 2,100 \text{ cm}^{-1}$ [38]. The
 3 relationship between absorption and the absorption coefficient (α) and thickness
 4 of the material (d) is as follows: $A(d) = 1 - \exp(-\alpha d)$. This expression shows
 5 the absorption is related to product of the α and d . It has been reported that
 6 suitable annealing temperature will increase the absorption coefficient of the
 7 GeSn film[15]. This is because the annealing accelerates the ion substitution
 8 process of Sn atoms in the Ge lattice and decreases the point defect. Additionally,
 9 the decrease of the compressive strain from the annealing will also enhance the
 10 absorption coefficient due to the shrink of the bandgap of the GeSn and ($\alpha \propto$
 11 $\sqrt{h\nu - E_g}$). The slightly reduced photon current (from 42.9 to 35.8 and 39.8 μA)
 12 might be mainly attributed to the decrease of the thickness of the GeSn. As
 13 discussed above, the SIMS results prove that the thickness of the GeSn layer
 14 decreases from 195 to 160 nm after the high-temperature annealing at 700°C.



15
 16 Figure 6. (a) and (b) Current-voltage characteristics of the optimized MSM
 17 photodetectors on the as-grown and annealed GeSn at 550°C with 5 minutes measured
 18 in dark and under illumination at 2 μm . (c) $I_{\text{photon}}/I_{\text{dark}}$ ratio with the relationship of the dark
 19 current at 0.5 V for 12 devices with the input light power of 23.21 mW.

20
 21 To further enhance the performance of the GeSn photodetector, the size of
 22 the device is decreased to obtain a lower dark current, while an optimized
 23 annealing temperature is used to enhance the photocurrent. The active area is
 24 composed of 1 μm interdigitated fingers with 4 μm spacings with a mesa area of
 25 $30 \times 35 \mu\text{m}^2$. The total junction area is decreased to $2 \times 10^{-4} \text{ cm}^2$. Annealing at
 26 550°C for 5 minutes was performed on the 8% Sn-content sample before device
 27 fabrication. The current-voltage curve of as-grown and annealed GeSn
 28 photodetector under dark and light conditions are shown in Figure 6(a) and (b).
 29 After annealing, an obvious difference between the dark and light current curve
 30 is observed compared to the as-grown sample. For a voltage of 0.5 V, the
 31 annealed device has a dark current of 3.6 μA and photocurrent of 4.9 μA (laser
 32 power, 23.21 mW), while the as-grown device has the dark and photocurrent of
 33 7.5 and 2.1 μA , respectively. This result indicates that the $I_{\text{p}}/I_{\text{d}}$ ratio increased
 34 from 0.28 to 1.36 with an obvious photocurrent increase (2.3 times) after an

1 annealing process. It should be noted that the dark, photon current and I_p/I_d ratio
2 of initial photodetector annealed at 700°C are 22.3 μA , 7.2 μA , and 0.32,
3 respectively, at 0.5 V (Figure 5 (d)). The obvious dark current decrease (22.3 to
4 7.5, 3.6 μA) with the decrease of the device size is in line with expectations. On
5 the other hand, the modified device (as-grown and annealed) has a low
6 photocurrent compared to the initial photodetector (2.1 and 4.9 μA compared to
7 7.2 μA). This is due to a large incident laser spot of a diameter larger than 50 μm ,
8 which is larger than the active area of the modified small device. Though the core
9 diameter of the used single-mode fiber is only $\sim 10 \mu\text{m}$, the incident spot on the
10 device might be larger than 50 μm as the incident light is divergent from a small
11 numerical aperture ~ 0.12 of the fiber [40].

12 To confirm the increase of photocurrent from annealing, twelve devices (six
13 as grown, six annealed at 550°C) were measured at 0.5 V with the input light
14 power of 23.21 mW. The result shows all annealed devices have a higher I_p/I_d
15 ratio than that of the as-grown sample (Figure 6(c)). The annealed devices have
16 an average I_p/I_d ratio around 0.87 which is 4.4 times of the as-grown device.
17 Furthermore, since the dark currents are on the same scale, the increase of I_p/I_d
18 ratio should be mainly due to the enhanced photocurrent (>2 times) by the
19 annealing process. This result indicates that more electron-hole pairs are created
20 in the thin film, which greatly increases photocurrent. This fact indicates the
21 positive effect of an optimized annealing process on the crystal quality of the
22 GeSn layers and their performance of photodetectors. This optimized
23 temperature is close to the previous GeSn annealing temperature which increases
24 photoluminescent intensities and light absorptions [15, 41]. Finally, it should be
25 pointed out for this annealed condition, the average dark current of the annealed
26 sample (4.08 μA) is slightly higher than that of the as-grown sample (3.01 μA).
27 A reasonable explanation might be the longer annealed time. More research is
28 required to study the effect of annealed time and temperature on the dark current
29 of the GeSn photodetector. For this work, at least, the annealed GeSn
30 photodetector with the same scale of the dark current demonstrates good
31 performance.

32 33 **4. Conclusion**

34 In summary, we have explored the thermal stability of GeSn epilayers with
35 different Sn contents ranging from 3 to 10% under a series of annealing
36 temperatures (300-750°C). It is observed that highly ordered nanopatterns are
37 formed on the crosshatch GeSn (8% Sn content) surface when the sample is
38 subjected to 700°C RTA for 1 min. These nanopatterns, which consist of ordered
39 3D-islands are attributed to the redistribution of surface adatoms due to the
40 inhomogeneities of the strain field caused by the periodic misfit dislocations.
41 Despite undergoing high-temperature RTA, these GeSn layers maintain the
42 crystal quality as confirmed by the XRD, Raman, and MSM photodetectors.
43 Furthermore, we also observed a significant photocurrent enhancement (>2 times)
44 when the annealing is carried out at 550°C for 5 min at the wavelength of 2 μm .

1 This work not only provides an approach to realize self-patterned 3D
2 nanostructures for further optoelectronic applications but also demonstrates a
3 suitable annealing process that will enhance the material quality and
4 photodetector performance of GeSn.

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9 **Conflicts of interest**

10 There are no conflicts to declare.

12 **Author Contributions**

13 Chuan Seng Tan and Shaoteng Wu designed the experiments. Lin Zhang and
14 Shaoteng Wu grew the samples. Shaoteng Wu and Bongkwon Son, Qimiao Chen,
15 Hao Zhou, and Simon Chun-Kiat Goh performed the characterizations. Shaoteng
16 Wu wrote the manuscript.

18 **Acknowledgments**

19 This work was support by the National Research Foundation Singapore
20 Competitive Research Programme under Grant NRF-CRP19-2017-01 and
21 Ministry of Education Tier-1 Project under Grant 2019-T1-002-040. We thank
22 the Silicon-Center of Excellence for the use of optical measurement equipment.

23 **Supplementary material**

24 Supplementary data to this article can be found online at

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